IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

First Named Inventor: Thomas J Heller JR    :    GROUP ART UNIT 2187

Pat Appln: Serial No. 10/603251:    Examiner: Kalpit Parikh

Filed: June 25, 2003    :    Date: February 26, 2008

Title: Multiprocessor System with Dynamic Cache Coherency Regions :

Attorney Docket POU920020123US1 :

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AMENDMENT (Responsive to OA Dated 11/26/2007)

EXPEDITED PROCESSING REQUESTED

Commissioner for Patents
Washington DC 20231

Sir:


A. Introductory Comments:

In reply to the Official action mailed 11/26/2007 Applicants respectfully request entry of the following corrected amendment to place the application in condition for allowance in light of the remarks and these amendments.(The Commissioner is also hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 09-0463. Those expected are already part of the separate fee authorization.)

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